Full Name of the Faculty Dr. Heli Amit Shah

Designation Associate Professor and Dean(R&D)

Email id <u>helishah.ee@bitseducampus.ac.in</u>

contact number

+91-9904444883

Total Experience

Teaching (years, months) 10 Years, 01 Month

Research (years, months) 03 Years, 06 Months

Professional Summary Working as an Associate Professor of Electrical engineering Department and

Dean (R&D) of BIT since 2016.

Worked at various positions like Paper setter at GTU, Life member of ISTE, Life member of Society of Power Engineers, Life Member of IEEE, Member of e-

Yantra Team BIT,

Current Activities Dean (R&D) at BIT, Looking after Start-up and Innovation activities at BIT

Associate Professor of the Electrical Engineering Department-BIT,

Member of e-Yantra (an initiative of MHRD) Team of BIT

Specialization Areas Power Electronics and Drives

Automatic Control and Robotics

Digital Signal Processing

Subject Taught (UG/PG) Control System Engineering

Signals and Systems Basic Electronics Digital Electronics

Advance Power Electronics

Qualifications Ph. D. (Electrical) - 2015

Sardar Vallabhbhai National Institute of Technology, Surat

M.E. (Electrical – Automatic Control and Robotics) – 2005

Faculty of Technology and Engineering, The M. S. University of Baroda,

Vadodara

B.E. (Electrical) -2003

Gujarat University, Ahmedabad

Skills Well versed in simulation software like MATLAB, P-SIM, word processing

software like LaTeX,

Basics of Arduino microcontroller and its applications,

Firebird-V robot and its programming through embedded-C (AVR studio)

Achievements Gold medal for securing first rank at M.E. (Electrical-ACR) at M.S. University

of Baroda - 2005

NIL

Research Project

Publications 1. "New Three-Dimensional Space Vector based Switching Signal

Generation Technique without Null Vectors and with Reduced Switching Losses for a Grid-Connected Four leg Inverter", IEEE

Transactions on Power Electronics, Vol. 31, No. 2, pp. 1026-1035

- 2. "Reactive Power Compensation in Three Phase Four Wire System Using Shunt Compensator with A Modified Three-Dimensional Space Vector Switching Technique", ACEEE International Journal of Electrical and Power Engineering
- 3. "A Modified Three-Dimensional Space Vector based PWM Method for Four-Leg Voltage Source Inverter Fed Asymmetrical Two-Phase Induction Motor" at IEEE PEDS 2011, Singapore
- 4. "Comparative techniques of Switching Signal Generation Techniques for Three Phase Four wire shunt Active Power Filters" at IEEE IEMDC 2011, Canada
- 5. "Simulation of Three Phase Four Wire Shunt Active Power Filter using Novel Switching Technique" at IEEE PEDES conference 2010

Activities and honours

Grant Received

Appointed as reviewer for papers submitted for IEEE Transactions on Transport Electrification (International Journal with Impact Factor of 4.7)

Rs 100000 worth robotic kits received on successful completion of e-yantra task-based training at IIT-Bombay